


RECEIVED
CENTRAL FAX CENTER
MAY 23 2005

Z&PINFP10802

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper for Serial No. 10/615,567 is being facsimile transmitted to the Patent and Trademark Office on the date shown below.


Signature

ALFRED K. DASSLER

May 23, 2005
Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Dietrich Bonart et al.
Applic. No. : 10/615,567 Confirmation No. 9016
Filed : July 8, 2003

Title : Vertical Transistor, and Method for Producing
a Vertical Transistor

Examiner : Mary A. Wilczewski
Group Art Unit : 2822
Docket No. : Z&PINFP10802
Customer No. : 24131

R E S P O N S E

Mail Stop Non-Fee Amendment
Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

Responsive to the Office action dated February 22, 2005 kindly consider the following remarks:

Remarks/Arguments begin on page 2 of this paper.